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TRANCMITTAL		Application Numbe	r	10/651,596	6					
TRANSMITTAL	Filing Date		August 29, 2003							
APR 2 7 2006 FORIVI	First Named Inventor		Daniel C. Biederman							
	Art Unit		2116							
RADEMAN (to be used for all correspondence after	Examiner Name	•	Brown, Mid	chael J.						
Total Number of Pages in This Submiss		Attorney Docket No	umber	CISCO-75	06 (032590-217)					
ENCLOSURES (check all that apply)										
Fee Transmittal Form			3)		After Allowance Communication to TC					
Fee Attached	Licensing	-related Papers			Communication to Board als and Interferences					
Amendment / Reply	Petition			Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)						
☐ After Final	Petition to Convert to a Provisional Application			Proprietary Information						
Affidavits/declaration(s)		Attorney, Revocation of Correspondence Add	ress	Status Letter						
Extension of Time Request	Terminal (Disclaimer		Other Enclosure(s) (please identify below):						
Express Abandonment Request	Express Abandonment Request			1 cited prior art reference PTO Form 1449						
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Reply to Missing Parts/										
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under 37 CFR1.52 or 1.53										
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Firm	THELEN REID	& PRIEST LLP								
Signature	Max									
Printed Name										
Date	Reg. No. 38,745									
	CERTIFICA	TE OF TRANSMISS	ION/MAI	LING						
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Signature Kare	Alloces	~								
Typed or printed name Karen A.				Date	April 24, 2006					

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 37 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

CANT:

Daniel C. Biederman

CONFIRMATION NO.: 8906

SERIAL NO.:

10/651,596

FILING DATE:

August 29, 2003

TITLE:

INLINE POWER BASED DEVICE COMMUNICATIONS

EXAMINER:

Brown, Michael J.

ART UNIT:

2116

CERTIFICATE OF MAILING

I hereby certify that this paper is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Mail Stop: Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date printed below:

Date: April 24, 2006

Mail Stop: Amendment

COMMISSIONER FOR PATENTS

P.O. Box 1450

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INFORMATION DISCLOSURE STATEMENT

In compliance with the duty of disclosure under 37 C.F.R. §§ 1.56, 1.97 and 1.98, applicant hereby submits this Information Disclosure Statement (IDS) including a Form PTO-1449 containing a list of references which may be material to the examination of the abovereferenced application. This submission is not to be construed as a representation that a prior art search has been conducted, that additional information material to the examination of this application does not exist, or that any one or more of the listed references constitutes prior art.

The Examiner is urged to carefully consider this Information Disclosure Statement and all the listed references, and to indicate same by initialing the appropriate portion(s) of the Form PTO-1449 and forwarding an initialed copy to applicant.

If this box | is checked, this application was filed after June 30, 2003 and qualifies for the blanket waiver of deposit of copies of U.S. Patents and U.S. Patent Application Publications

Docket No.: CISCO-7506 (032590-217)

in accordance with the written waiver of 37 CFR §1.98 (a)(2)(i) Official Gazette Published in Official Gazette Notices of August 5, 2003 (1273OG55). Accordingly, such copies are not submitted.

	I
This statemen	nt is filed pursuant to (CHECK ONE BOX):
	37 C.F.R. § 1.97(b). This information disclosure statement is filed either: (1) within three months of the filing date of a national application other than a continued prosecution application under §1.53(d); (2) within three months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application; (3) before the mailing date of a first office action on the merits; or (4) before the mailing of a first office action after the filing of a Request for Continued Examination under 37 C.F.R. §1.114, whichever event occurs last.
	Accordingly, this information disclosure statement requires no fee and no certification.
	37 C.F.R. § 1.97(c). This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97(b), but before the mailing date of any of the following: (1) a final action under 37 C.F.R. § 1.113; (2) a notice of allowance under 37 C.F.R. § 1.311; or (3) an action that otherwise closes prosecution in the application.
	Accordingly, this information disclosure statement requires either: (1) the fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c); or (2) a certification according to 37 C.F.R. § 1.97 (e)(1) or (2).
	37 C.F.R. § 1.97(d). This information disclosure statement is filed after the period specified in 37 C.F.R. § 1.97 (c). Accordingly, this information disclosure statement requires: (1) a certification in accordance with 37 C.F.R. § 1.97(e); and (2) the fee specified in 37 C.F.R. § 1.17 (p) to consider an information disclosure

If this statement crosses in the mail with an office action, or is otherwise not in the indicated category of 37 C.F.R. §1.97, it is respectfully requested that this statement be treated in the next appropriate category and made of record. To the extent required, please treat this paper as a conditional petition for acceptance of the information disclosure statement.

statement under 37 C.F.R. § 1.97(d).

Docket No.: CISCO-7506 (032590-217)

II Fees Due (CHECK ONE BOX): No fee is due. The fee specified in 37 C.F.R. § 1.17(p) for submission of an information disclosure statement under 37 C.F.R. § 1.97(c) or 37 C.F.R. § 1.97(d) is enclosed (\$180). Ш Certification (CHECK ONE BOX): No certification is necessary. Pursuant to 37 C.F.R. § 1.97(e)(1), the undersigned hereby certifies: That each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement. Pursuant to 37 C.F.R. § 1.97(e)(2), the undersigned hereby certifies: No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this information disclosure statement was known to any individual designated in 37 C.F.R. \$1.56(c) more than three months prior to the filing of this information disclosure statement. IV Please charge any additional required fee or credit any overpayment to our deposit account number 50-1698. Respectfully submitted, THELEN REID & PRIEST LLP Dated: April 24, 2006

THELEN REID & PRIEST LLP P.O. Box 640640 San Jose, CA 95164-0640

Tel: (408) 292-5800 Fax: (408) 287-8040 Reg. No. 38,745

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Form PTO 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Fratemark Office				Atty. Docket No. Serial No. 10/651,596					
Information Disclosure Statement by Applicant				Applicant: Daniel C. Biederman					
(Use several sheets if necessary)				Filed: 8/29/2003 Group: 2116					
			U.S. Patent Doc	uments				_	
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	Kiss, Peter, "Cha								
		Analog Circuit Imperfections for Cascaded Delta-Sigma Analog-to-Digital Converters," Center for Design of Analog-Digital Integrated Circuits, Oregon State Univ., Dec. 31, 1999, pp.45-58.							
-	Design of Anal	og-Digital In	tegrated Circuits,	Oregon State	Univ., Dec	c. 31, 1999, pp.	45-58.		
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Examiner					Date Cons	idered			
	Initial if citation conside formance and not conside						rough cit	ation if	